

Appl. No. 09/983,092
Amdt. Dated December 7, 2004
Reply to Office action of October 19, 2004
Attorney Docket No. P06752-2/004080-171
EUS/J/P/04-6275

Amendments to the Claims:

This listing of Claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1-17. (Cancelled)

18. (Currently Amended) A mini cell header reading device for extracting, from a user data channel ~~the~~ user data part of an individual connection, comprising a shift register into which ~~the bit~~ a bit stream of the user data channel is shifted in synchronism with clock pulses, a first counter counting ~~the size~~ a size of a cell size indicating field in ~~the header~~ a header of the mini cell shifted into the shift register in synchronism with said clock pulses, a latch register connected to the first counter and to the shift register to latch ~~the information~~ an information resident in the cell size indicating field as counted by the first counter, a memory connected to said latch register, a second counter connected to the latch register and ~~the memory~~ a memory for controlling a multiplexor so that the user data part of the mini cell in the shift register is extracted from said user data bit stream, ~~characterized in that the~~ wherein said information in the latch register is used as address to the memory, and that at said address the size of the user data part is stored.

19. (Currently Amended) A cell header reading device in accordance with claim 18 ~~characterized in wherein~~ that said memory is a ROM memory in which there is mapped at each address received from said latch register an individual cell size.

20. (Currently Amended) A cell header reading device in accordance with claim 19, wherein there is a control system for controlling set up and release of connections in a mobile telephone system ~~characterized in that~~ wherein said memory is a RAM memory in which said control system writes at each address received from said latch register an individual cell size.

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21. (Currently Amended) A mobile telephone system comprising an ATM network to which a sending unit (201) and a receiving unit (202) are connected over a respective link (205, 206), said sending device unit comprising means for multiplexing mini cells from user data sources (203) into a user data stream, said receiving device unit receiving a user the user data stream from said ATM network, said ~~latter~~ user data stream comprising mini cells which belong to connections that are to be terminated by user data ~~sinks~~ links (204) connected to the receiving device, ~~characterized in that wherein~~ said receiving device (202) unit comprises a first cell header reading device (208).

22. (Currently Amended) A mobile telephone system in accordance with claim 21, ~~characterized in that said wherein~~ sending device unit comprises a second cell header reading device (207).

23. (Currently Amended) A mobile telephone system in accordance with claim 22, wherein said first cell header reading device comprises a shift register into which said user data stream is shifted in synchronism with clock pulses, a first counter counting ~~the~~ size of a cell size indicating field in ~~the~~ header of the mini cell shifted into the shift register in synchronism with said clock pulses, a latch register connected to the first counter and to the shift register to latch the information resident in the cell size indicating field as counted by the first counter, a memory connected to said latch register, a second counter connected to the latch register and the memory for controlling a multiplexor so that the user part of the mini cell in the shift register is extracted from said ~~bit~~ data stream, ~~characterized in that wherein~~ the information in the latch register is used as address to the memory, and that at said address the size of the user data part is stored.

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24. (Currently Amended) A mobile telephone system in accordance with claim 23 ~~characterized in that~~ wherein said memory is a ROM memory in which there is mapped at each address received from said latch register an individual cell size.

25. (Currently Amended) A mobile telephone system in accordance with claim 24, wherein there is a control system for controlling set up and release of connections in the mobile telephone system ~~characterized in that~~ wherein said memory is a RAM memory in which said control system writes at each address received from said latch register an individual cell size.